

Indirect Model Predictive Control for Capacitor Energy Control in Modular Multilevel Converters

Leonardo Testa^{1,2} Petros Karamanakos² Christoph M. Hackl^{1,†}

¹Hochschule München (HM) University of Applied Sciences

²Faculty of Information Technology and Communication Sciences, Tampere University

Motivation

Research questions:

- Which nonlinearities are present in the total capacitor energy dynamics of the MMC?
- What impact do the nonlinearities in MMC dynamics have on the performance and constraint satisfaction of linear MPC controllers?

A linear MPC in a cascaded structure is proposed to mitigate the effects of the nonlinearities.

System Description

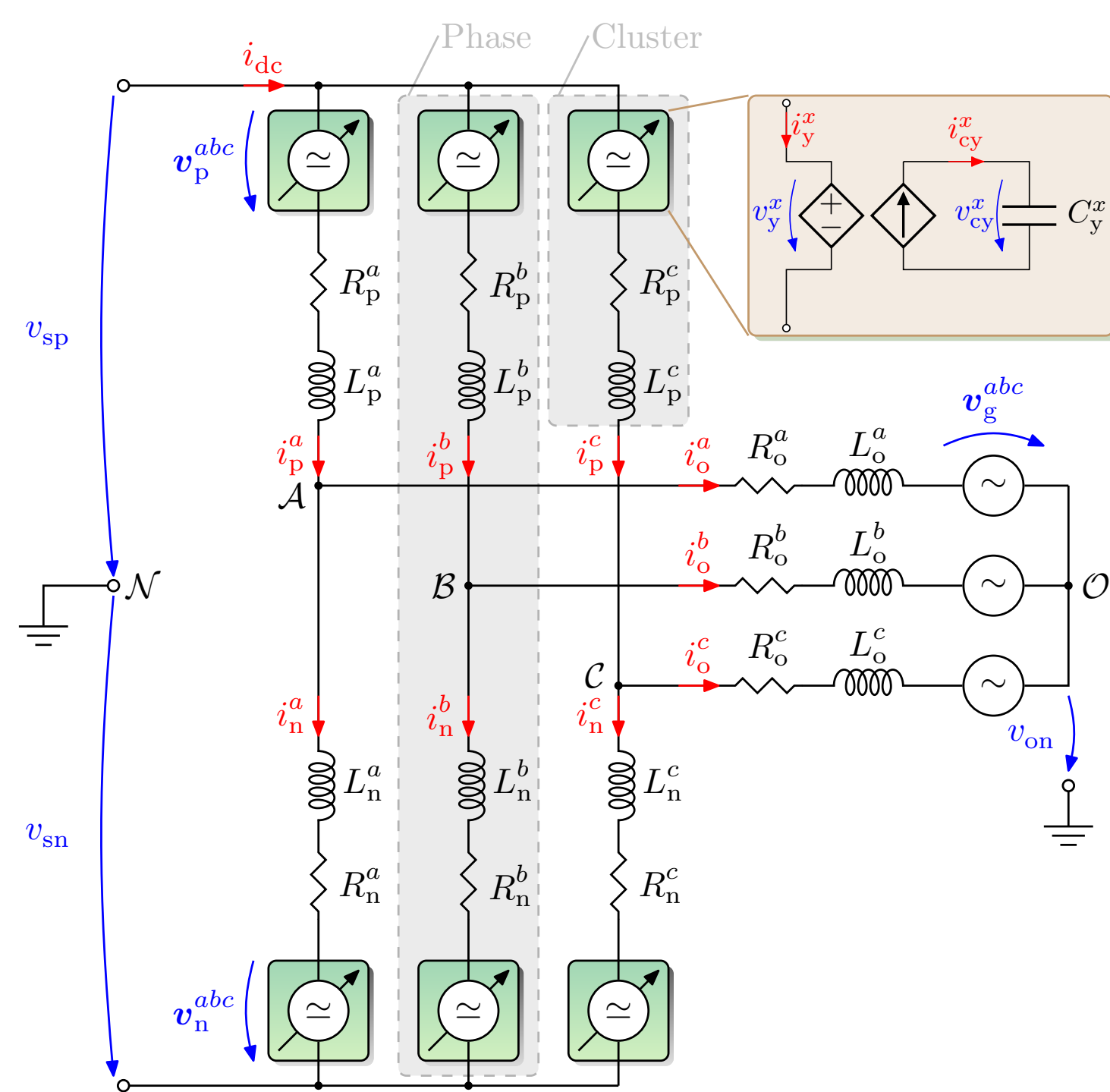


Figure 1. Average value model of the investigated MMC in double-star connection.

Decomposition of cluster currents into grid currents, circulating currents, and dc current.

$$\frac{d}{dt} \mathbf{i}_{\Sigma\Delta}^{\alpha\beta\gamma} = (\mathbf{L}_{\Sigma\Delta}^{\alpha\beta\gamma})^{-1} \left(-\mathbf{R}_{\Sigma\Delta}^{\alpha\beta\gamma} \mathbf{i}_{\Sigma\Delta}^{\alpha\beta\gamma} - \mathbf{v}_{\Sigma\Delta}^{\alpha\beta\gamma} + \begin{pmatrix} \mathbf{0}_3 \\ -2\mathbf{v}_g^{\alpha\beta\gamma} \end{pmatrix} + \frac{v_{dc}}{2} \begin{pmatrix} \mathbf{T}_c \mathbf{1}_3 \\ \mathbf{0}_3 \end{pmatrix} + \begin{pmatrix} \mathbf{0}_3 \\ -2v_{on} \mathbf{T}_c \mathbf{1}_3 \end{pmatrix} \right) \quad (1)$$

Nonlinear capacitor energy dynamics.

$$\begin{aligned} \frac{d}{dt} e_{c,\Sigma}^{\gamma} = & -\frac{L}{2} \left(\frac{d}{dt} \mathbf{i}_{\Sigma}^{\alpha\beta} \right)^{\top} \mathbf{i}_{\Sigma}^{\alpha\beta} - \frac{R}{2} \|\mathbf{i}_{\Sigma}^{\alpha\beta}\|^2 - L \mathbf{i}_{\Sigma}^{\gamma} \frac{d}{dt} \mathbf{i}_{\Sigma}^{\gamma} - R (\mathbf{i}_{\Sigma}^{\gamma})^2 \\ & - \frac{2L_o + L}{4} \left(\frac{d}{dt} \mathbf{i}_{\Delta}^{\alpha\beta} \right)^{\top} \mathbf{i}_{\Delta}^{\alpha\beta} - \frac{2R_o + R}{4} \|\mathbf{i}_{\Delta}^{\alpha\beta}\|^2 + \frac{1}{2} v_{dc} \mathbf{i}_{\Sigma}^{\gamma} - \frac{1}{4} (\mathbf{v}_g^{\alpha\beta})^{\top} \mathbf{i}_{\Delta}^{\alpha\beta} \end{aligned} \quad (2)$$

Assumptions

- Assumption 1** All cluster parameters are identical and a symmetrical three-phase grid is considered.
- Assumption 2** The voltage drop across resistors and inductors is small enough to be negligible, i.e., $L_{\kappa} \frac{d}{dt} i_y^x + R_{\kappa} i_y^x \approx 0$.

Linear capacitor energy dynamics under Assumptions 1 and 2.

$$\frac{d}{dt} e_{c,\Sigma}^{\gamma} = \frac{1}{2} v_{dc} \mathbf{i}_{\Sigma}^{\gamma} - \frac{1}{4} (\mathbf{v}_g^{\alpha\beta})^{\top} \mathbf{i}_{\Delta}^{\alpha\beta} \quad (3)$$

Indirect MPC as QP

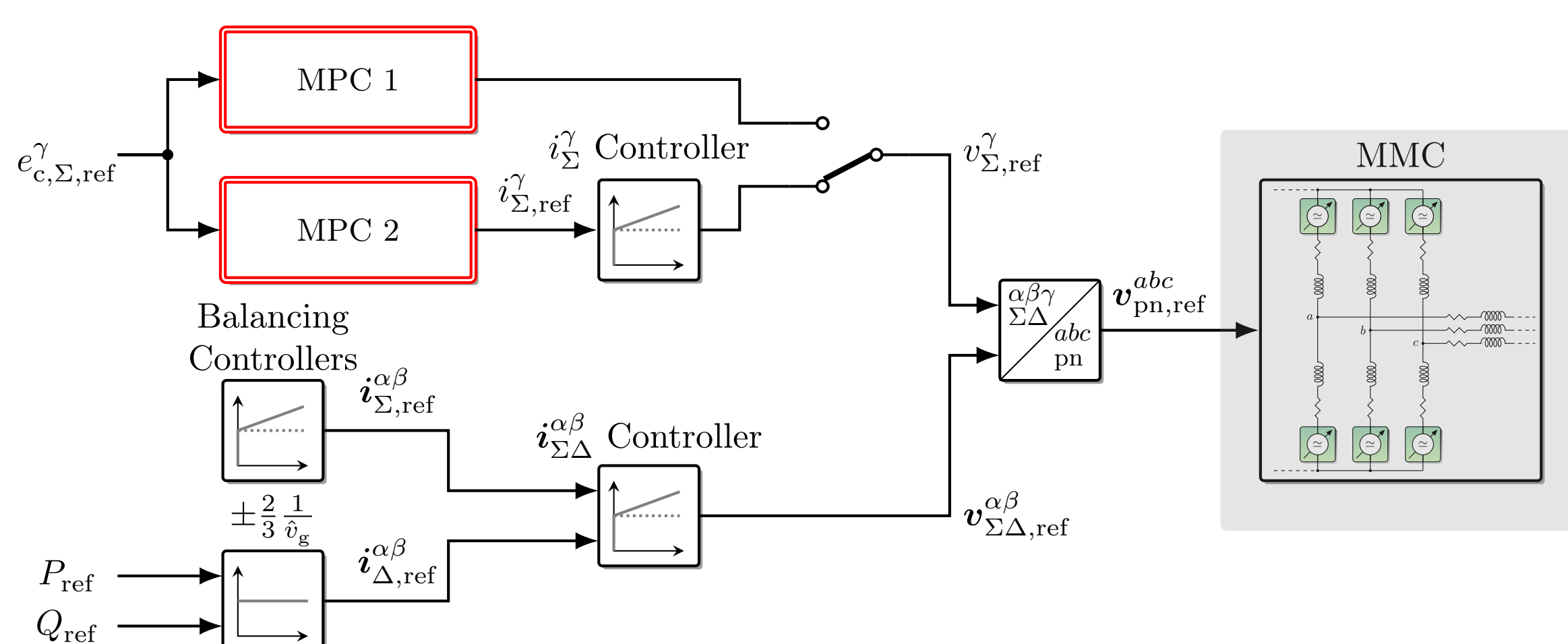


Figure 2. Control structure of the MMC with the proposed MPC variants MPC 1 and MPC 2 highlighted in red.

A generic MPC problem formulation considers discrete linear time-invariant (LTI) systems of the form

$$\begin{aligned} \mathbf{x}[k+1] &= \mathbf{A}\mathbf{x}[k] + \mathbf{B}\mathbf{u}[k] + \mathbf{d}[k], \quad \mathbf{x}[0] = \mathbf{x}_0 \\ \mathbf{y}[k] &= \mathbf{C}\mathbf{x}[k], \end{aligned} \quad (4)$$

with the cost function

$$J(k) = \sum_{l=k}^{k+N_p-1} \|\mathbf{y}_{\text{ref}}[l+1] - \mathbf{y}[l+1]\|_Q^2 + \|\Delta \mathbf{u}[l]\|_P^2 \quad (5)$$

A. MPC in Single-Loop Structure (MPC 1)

The total energy of the capacitors is controlled directly by computing the reference cluster voltage $v_{\Sigma,\text{ref}}^{\gamma}$, see Fig. 2.

$$\begin{aligned} \begin{pmatrix} e_{c,\Sigma}^{\gamma}[k+1] \\ i_{\Sigma}^{\gamma}[k+1] \end{pmatrix} &= \underbrace{\begin{bmatrix} 1 & \frac{1}{2}T_s v_{dc} \\ 0 & 1 - \frac{T_s R}{L} \end{bmatrix}}_{=\mathbf{A}} \begin{pmatrix} e_{c,\Sigma}^{\gamma}[k] \\ i_{\Sigma}^{\gamma}[k] \end{pmatrix} + \underbrace{\begin{pmatrix} 0 \\ -\frac{T_s}{L} \end{pmatrix}}_{=\mathbf{b}} v_{\Sigma,\text{ref}}^{\gamma}[k] + \underbrace{\begin{pmatrix} T_s p_{ac}[k] \\ \frac{T_s}{2L} v_{dc} \end{pmatrix}}_{=\mathbf{d}[k]}, \\ \mathbf{y}[k] &= \underbrace{(1, 0)}_{=\mathbf{c}^{\top}} \mathbf{x}[k], \end{aligned} \quad (6)$$

B. MPC in Cascaded Structure (MPC 2)

The total energy of the capacitors is controlled by computing a reference current, which is passed to a current controller, see Fig. 2.

$$\begin{aligned} \begin{pmatrix} e_{c,\Sigma}^{\gamma}[k+1] \\ i_{\Sigma}^{\gamma}[k+1] \end{pmatrix} &= \underbrace{\begin{bmatrix} 1 & \frac{1}{2}T_s v_{dc} \\ 0 & 1 - \frac{T_s}{T_{\sigma}} \end{bmatrix}}_{=\mathbf{A}} \begin{pmatrix} e_{c,\Sigma}^{\gamma}[k] \\ i_{\Sigma}^{\gamma}[k] \end{pmatrix} + \underbrace{\begin{pmatrix} 0 \\ \frac{T_s}{T_{\sigma}} \end{pmatrix}}_{=\mathbf{b}} i_{\Sigma,\text{ref}}^{\gamma}[k] + \underbrace{\begin{pmatrix} T_s p_{ac}[k] \\ 0 \end{pmatrix}}_{=\mathbf{d}[k]}, \\ \mathbf{y}[k] &= \underbrace{(1, 0)}_{=\mathbf{c}^{\top}} \mathbf{x}[k]. \end{aligned} \quad (7)$$

Validation and Simulation Results

Table 1. System and control parameters.

Parameter	MMC	p.u.
dc voltage V_{dc}	100 kV	2.5
rated apparent power S_R	60 MVA	1
grid voltage $\hat{v}_g$	25 kV	1
grid frequency f_g	50 Hz	1
grid side resistance R_o	0.1	0.0025
grid side inductance L_o	2 mH	0.0157
cluster capacitance C	1 mF	12.566
cluster resistance R_{pn}	0.1	0.025
cluster inductance L_{pn}	1 mH	0.00785
dc current limit $i_{\Sigma,\text{max}}^{\gamma} = -i_{\Sigma,\text{min}}^{\gamma}$	500 A	0.5
sampling interval T_s	100 μ s	0.0314
prediction horizon steps N_p		3

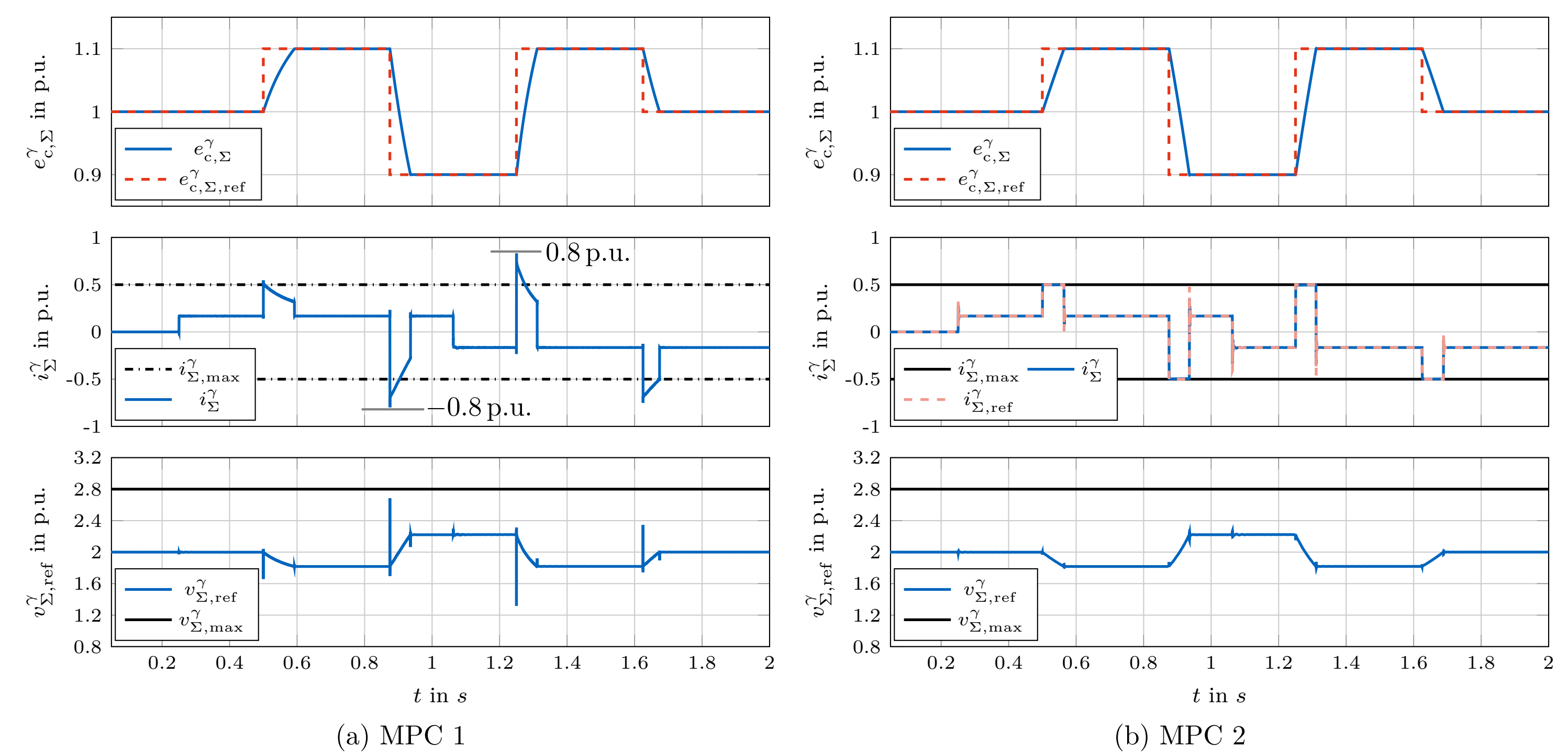


Figure 3. Performance of MPC 1 and MPC 2 with soft state (dash-dotted black line) and hard input (solid black line) constraints.

Conclusion and Future Work

- The nonlinearities in the energy dynamics are mainly due to the voltage drop over the resistors and inductors
- These lead to constraint violations for linear MPC in a single-loop structure. However, with a cascaded structure, constraints can be met.
- Extend results to include capacitor energy balancing in the energy control
- Investigate nonlinear MPC for capacitor energy control

References

- [1] Leonardo Testa, Petros Karamanakos, and Christoph M. Hackl. Indirect model predictive control for capacitor energy control in modular multilevel converters. In *proceedings of the 2025 IECON Annual conference of the IEEE Industrial Electronics Society*, 2025.

